

WEST Search History

DATE: Wednesday, July 21, 2004

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L19	(length adj field) and (attribute adj field) and (target adj field)	5
<input type="checkbox"/>	L18	VLIW and (length adj field)	42
<input type="checkbox"/>	L17	invalid adj3 opcode\$	148
<input type="checkbox"/>	L16	opcode adj compare	20
<input type="checkbox"/>	L15	opcode same compare	516
<input type="checkbox"/>	L14	(programmable adj3 processor) and (redefin\$ and instruction)	182
<input type="checkbox"/>	L13	programmable same processor same redefin\$ same instruction	3
<input type="checkbox"/>	L12	712/\$.ccls. and (instruction adj3 convert\$)	558
<input type="checkbox"/>	L11	redefin\$ same hardware same instruction?	34
<input type="checkbox"/>	L10	redefin\$ and hardware and instruction?	2750
<input type="checkbox"/>	L9	system adj response adj error\$	91
<input type="checkbox"/>	L8	(specification same change\$) and (design same error\$)	3251
<input type="checkbox"/>	L7	(transaction same redirection) and (specification adj change\$)	10
<input type="checkbox"/>	L6	transaction same redirection	220
<input type="checkbox"/>	L5	712/15.ccls.	179
<input type="checkbox"/>	L4	712/226.ccls.	317
<input type="checkbox"/>	L3	712/209.ccls.	278
<input type="checkbox"/>	L2	712/245.ccls.	600
<input type="checkbox"/>	L1	712/244.ccls.	447

END OF SEARCH HISTORY